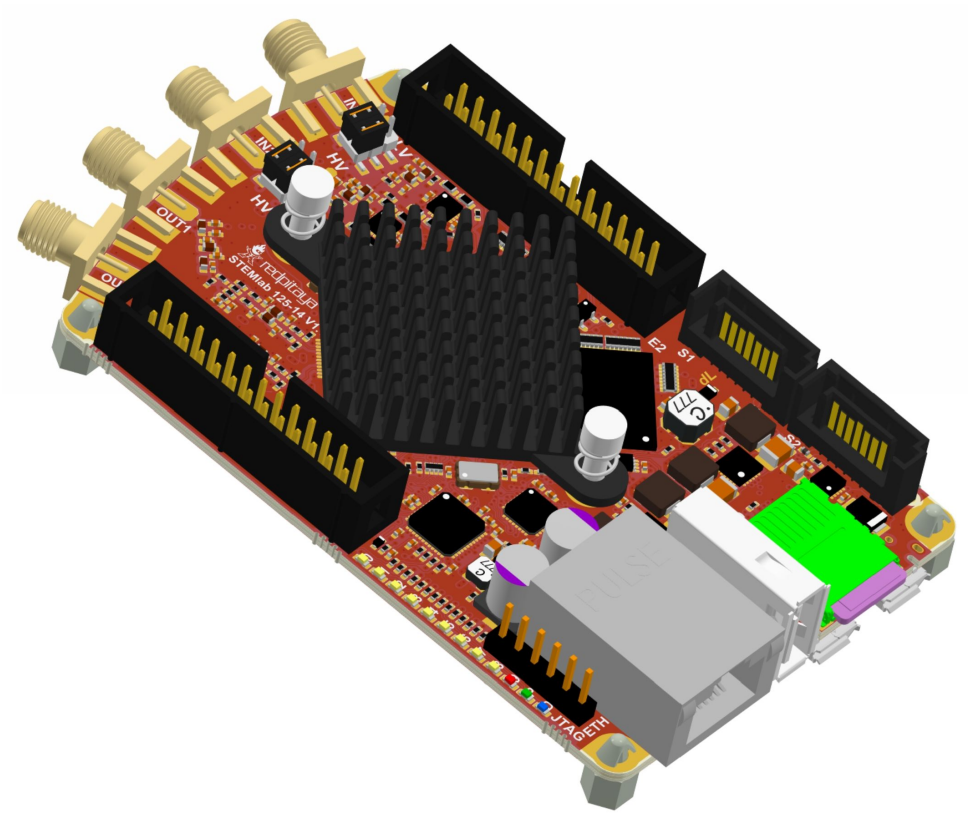


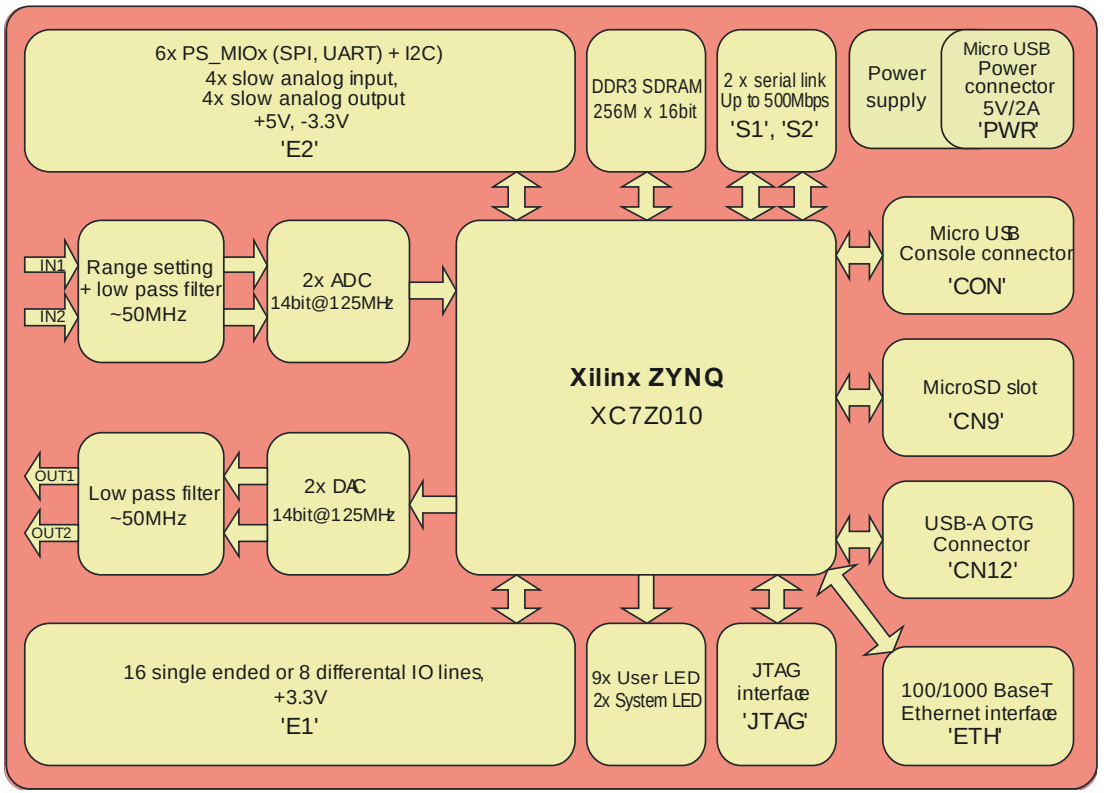
Red Pitaya d.o.o.
Velika pot 22
5250 Solkan
Slovenia

www.redpitaya.com

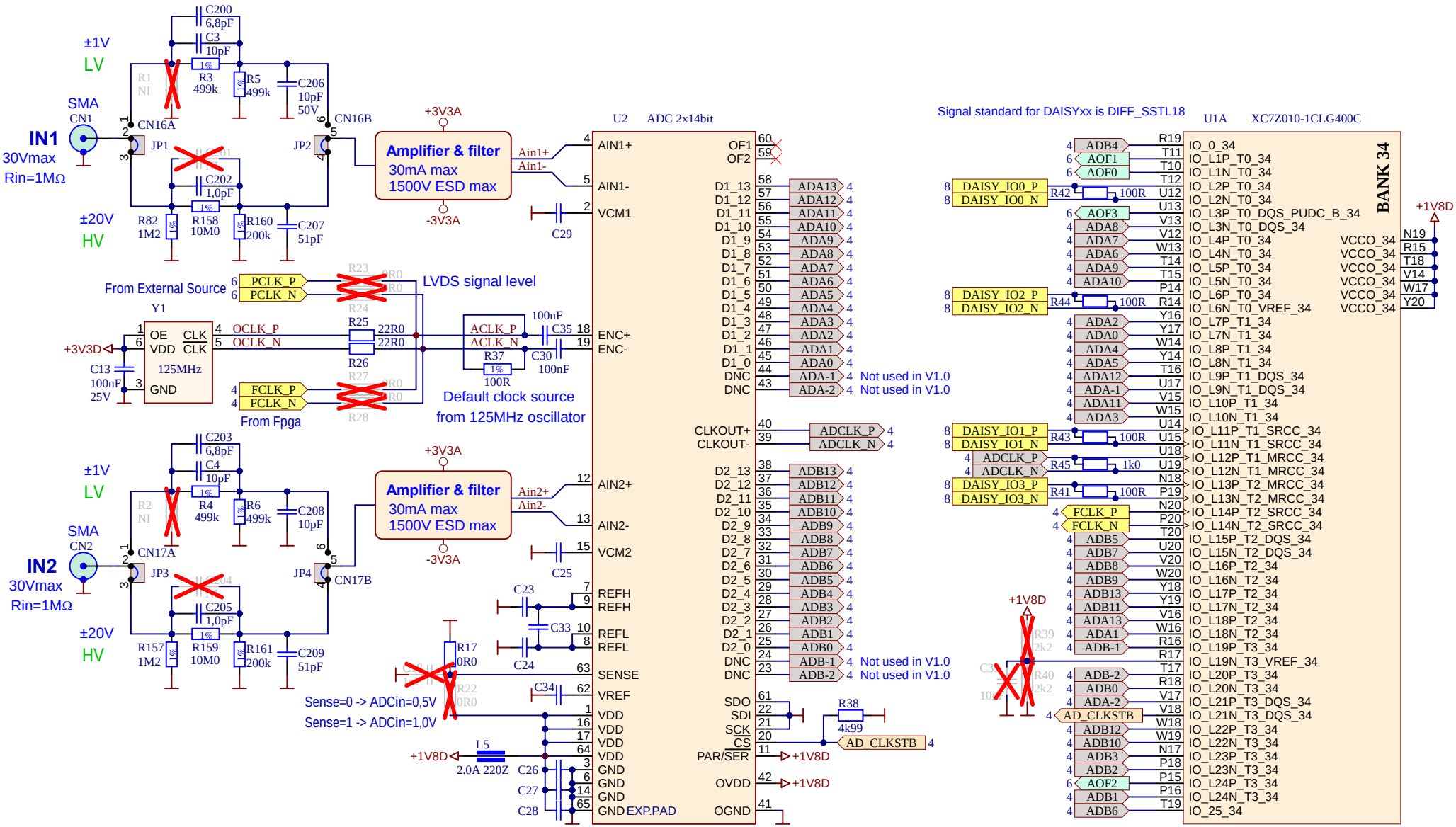
Electrical schematics for:

product: ***STEMlab_125-14***
version: ***V1.1***
variant: ***STEMlab 125-14***
release date: ***13. 9. 2021***

Red Pitaya STEMlab 125-14 V1.1 block schematics

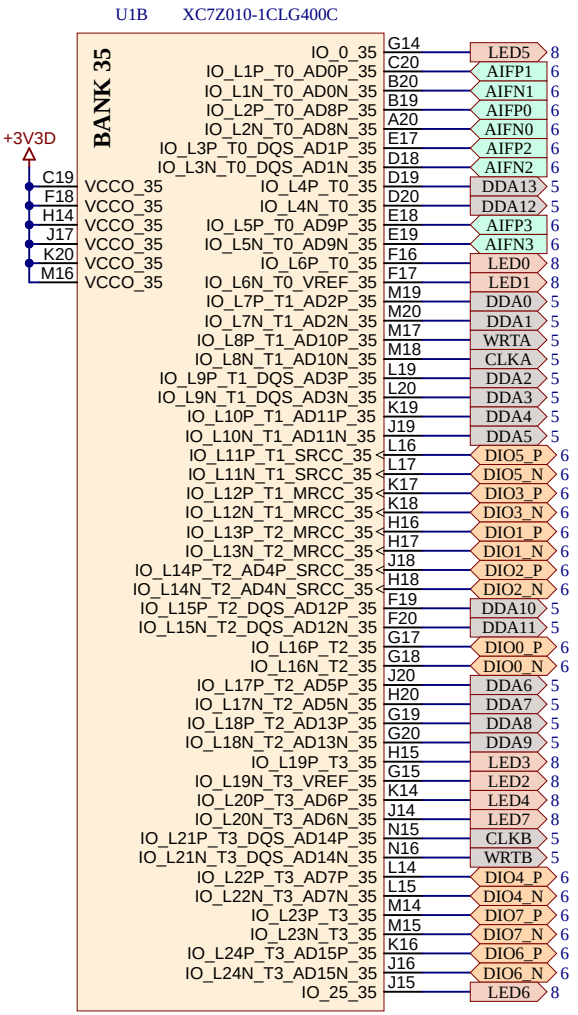


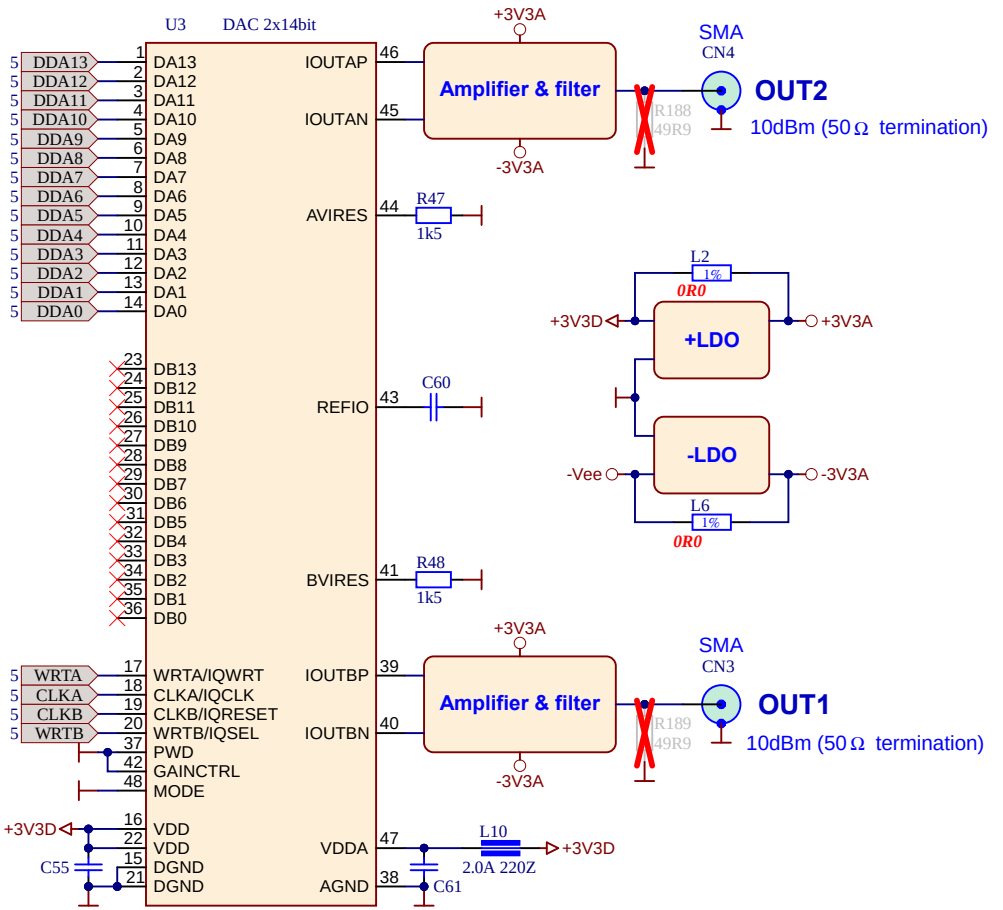
2 Analog front-end and AD converter, Zynq bank 34



Note: number next to port symbol indicates the sheet where the signal is connected

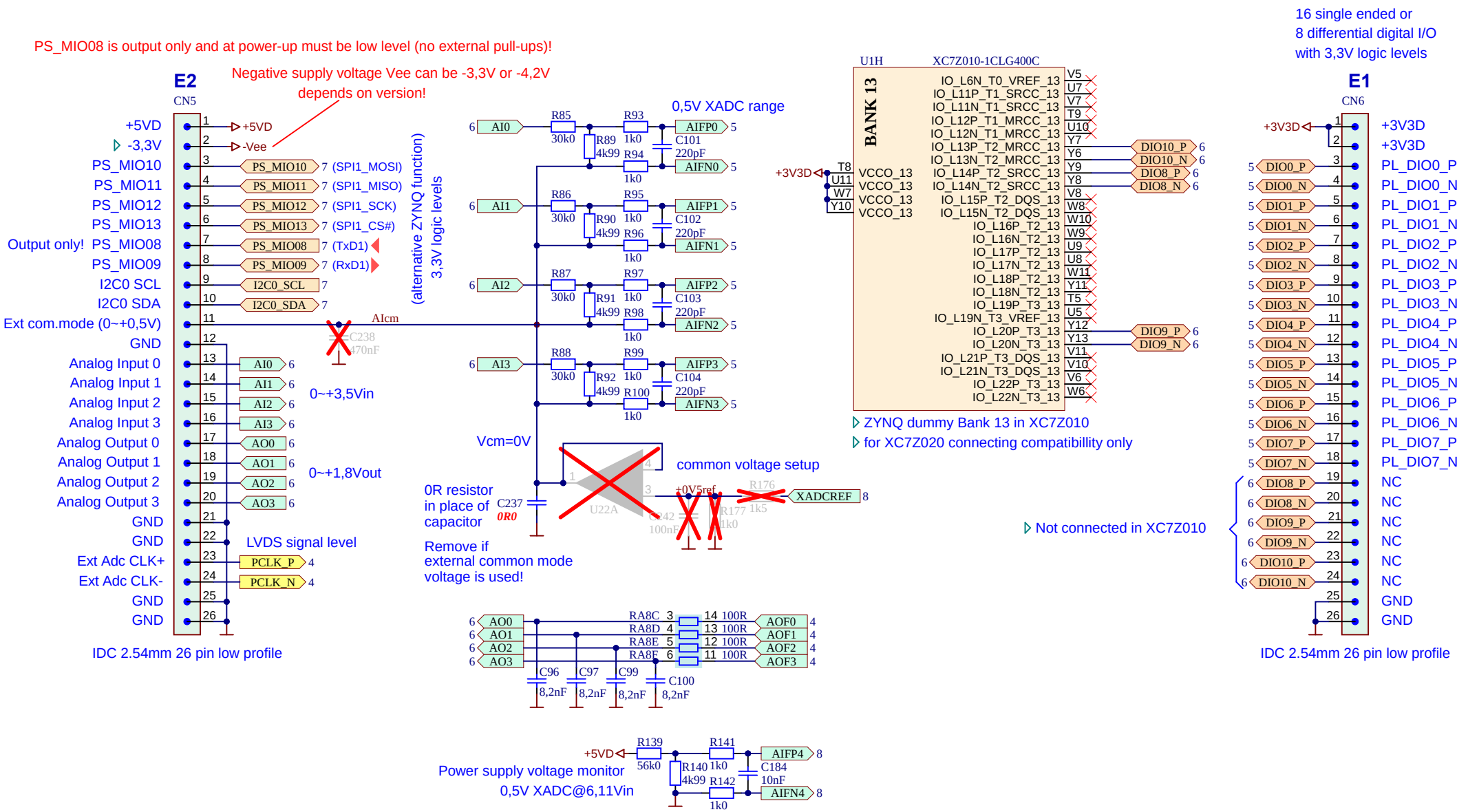
3 DA converter and analog back-end, Zynq bank 35





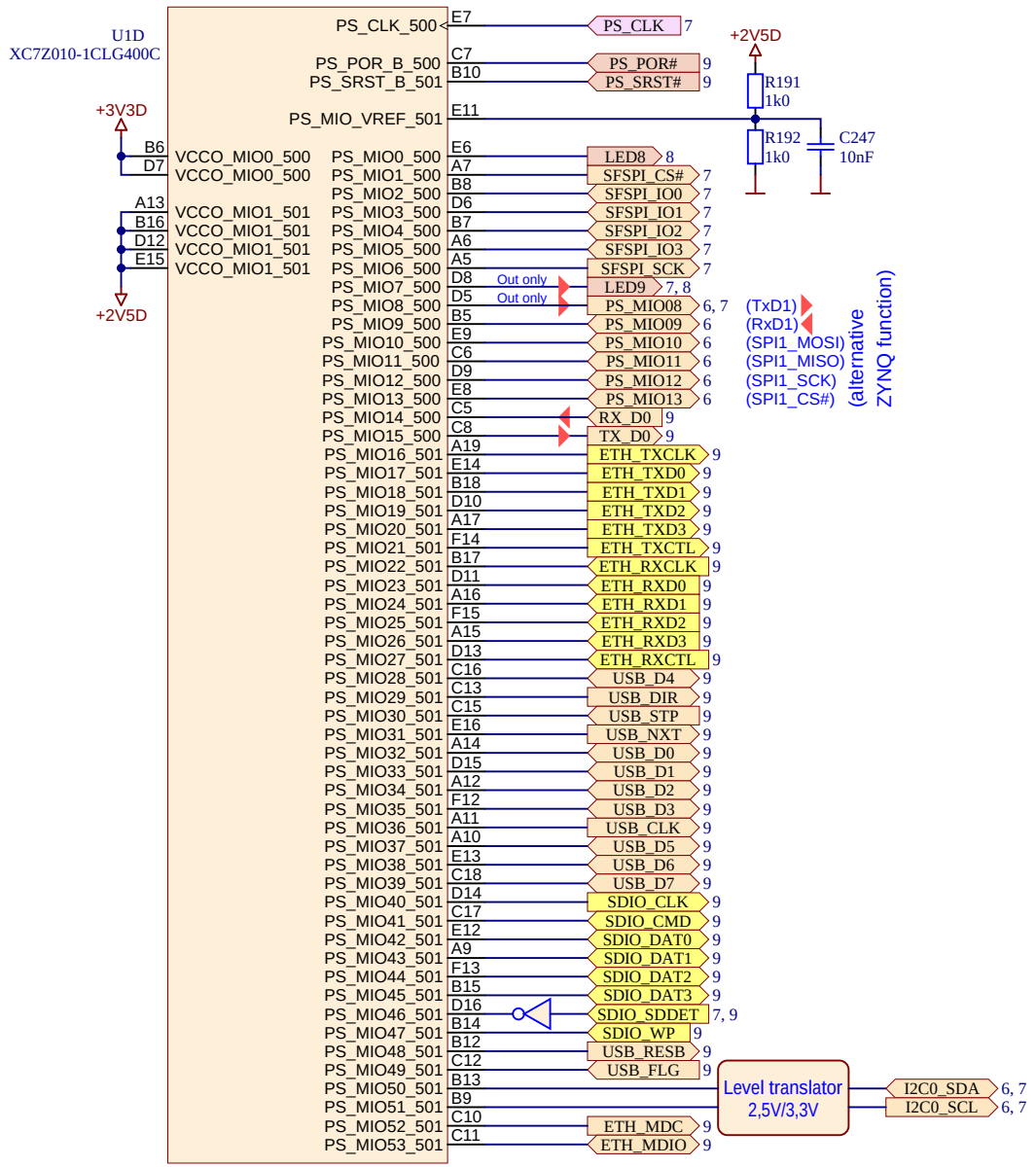
Note: number next to port symbol indicates the sheet where the signal is connected

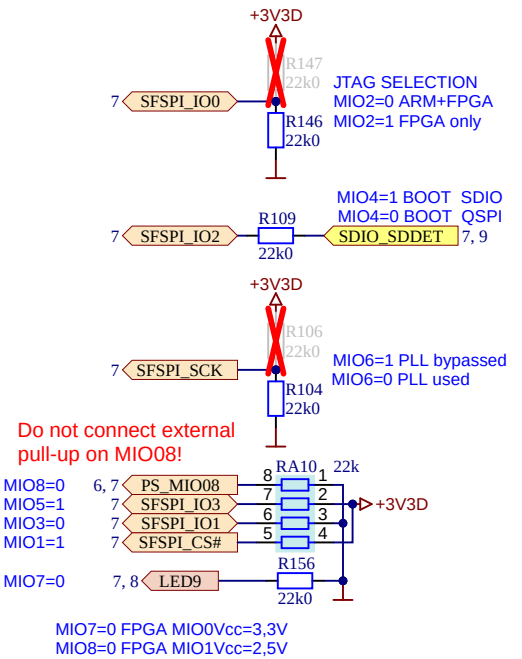
4 Digital and analog slow I/O, Zynq bank 13

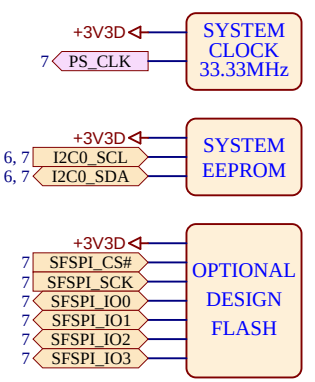


Note: number next to port symbol indicates the sheet where the signal is connected

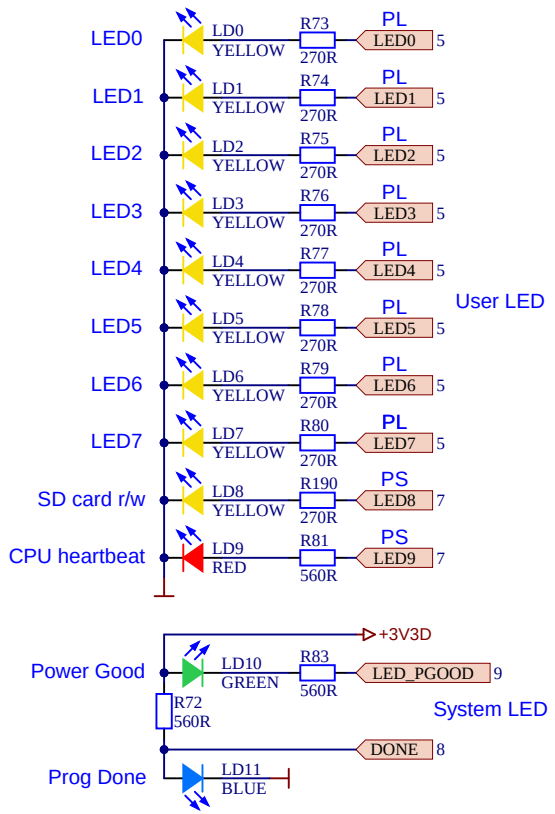
5 Zynq bank 500 and 501 (PS)

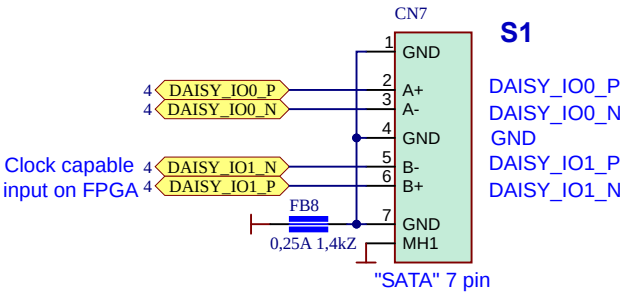


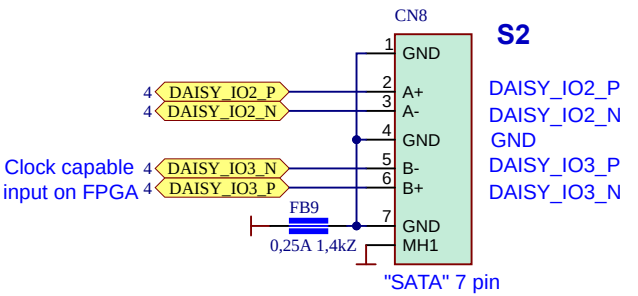


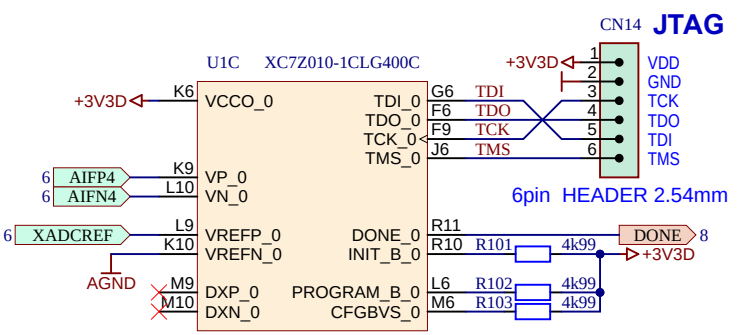


6 LED, Serial interface, JTAG



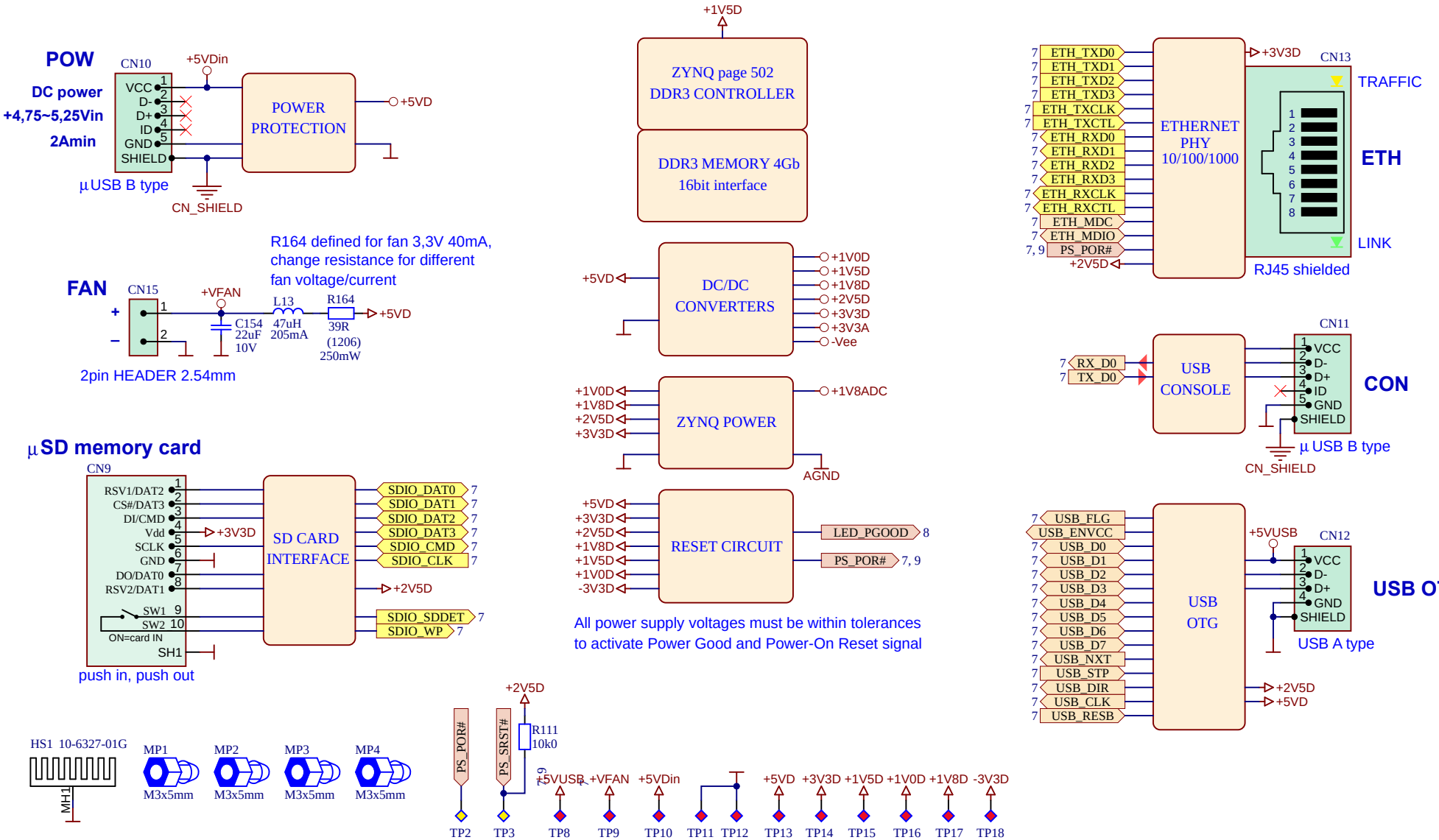






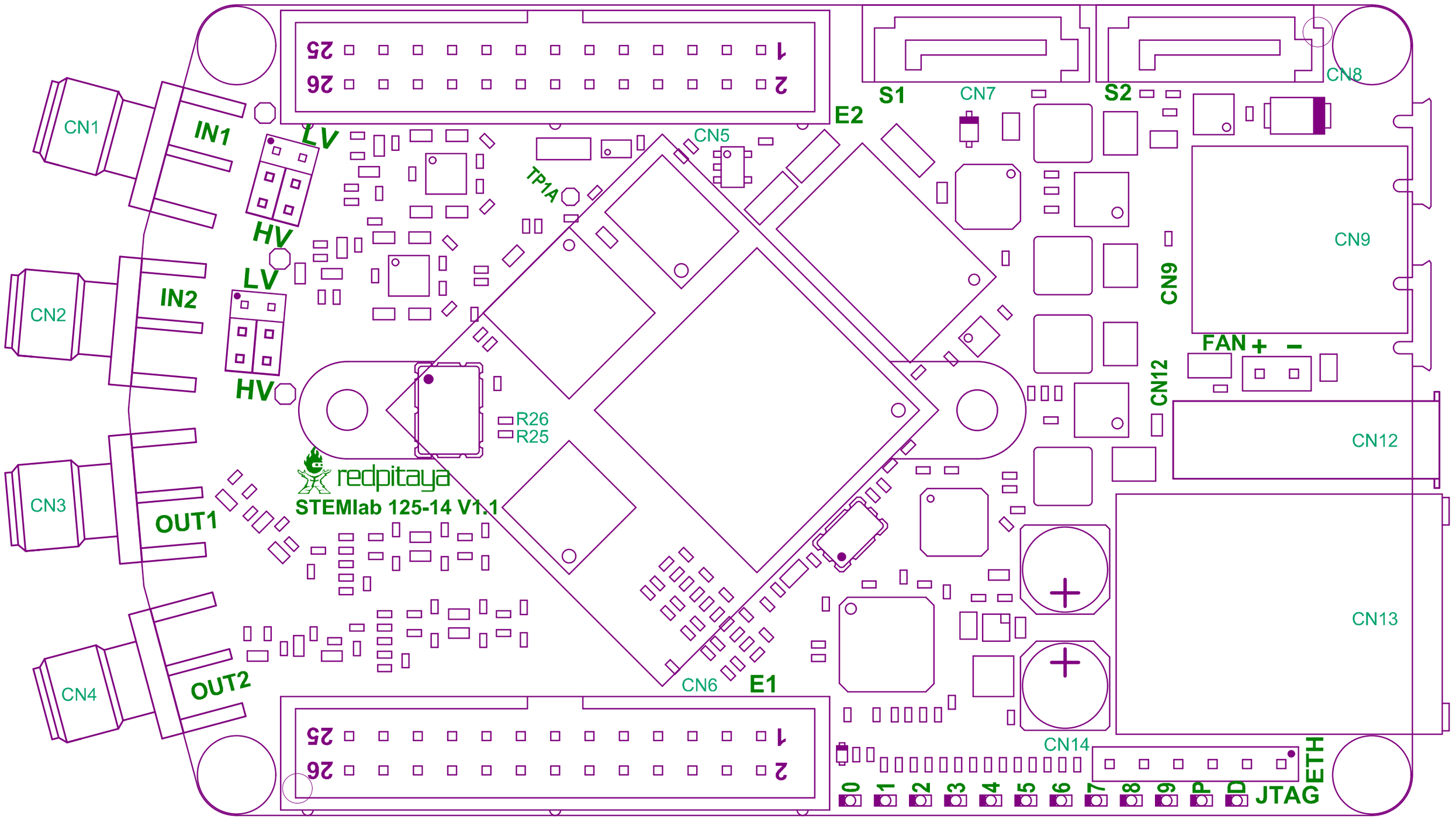
Note: number next to port symbol indicates the sheet where the signal is connected

7 Ethernet, memory card, Console, USB OTG, DDR3, Power



Note: number next to port symbol indicates the sheet where the signal is connected

Top assembly: STEMlab_125-14_V1.1 variant: STEMlab 125-14



Bottom assembly: STEMlab_125-14_V1.1 variant: STEMlab 125-14