

Notes:

1. Board shall be fabricated - performance class II as per IPC-6011 and IPC6012
2. PCB manufacturer logo, P/N, revision and/or date code of manufacturing shall be printed in top solder mask (not over pcb traces, allowed over copper plane). The date code shall be in the format: "WWYY" where WW=week and YY= year, max height 0.15 inches
3. Silkscreen printed on both sides; color= WHITE
4. Material: high temperature FR4 class epoxy glass
min 35um copper for all layers
Must be RoHS compliant and survive a lead-free assembly max reflow of 260 deg C (5 passes)
Td rating: >340 deg C
Tg = 150 deg C (min)
5. Solder mask: SMOBC per IPC-SM-840C, class T must be RoHS compliant, 0.001" max measured over bare copper plating, must clear all lands as indicated on gerber solder mask layers, color= GREEN
6. Finish: HAL-Sn100CL 7-10um over bare copper only
7. Solderability test: Category 2 of J-STD-003
8. Finished boards shall not have nicks, scratches, voids, exposed copper, poor plating or misdrilled holes
9. All holes sizes are after plating
10. All finished boards are to be 100% electrically tested
11. Unless otherwise indicated, all linear tolerances shall be XX.X +/-0.2mm and XX.XX +/- 0.1mm
12. Gerber file GM1 shows board outline (milling line)
13. Panelization: 2x2 (160,6x137mm)

Additional notes:


- A1. Finished board thickness = 1.6mm +/- 10%; measured over top/bottom copper and solder mask

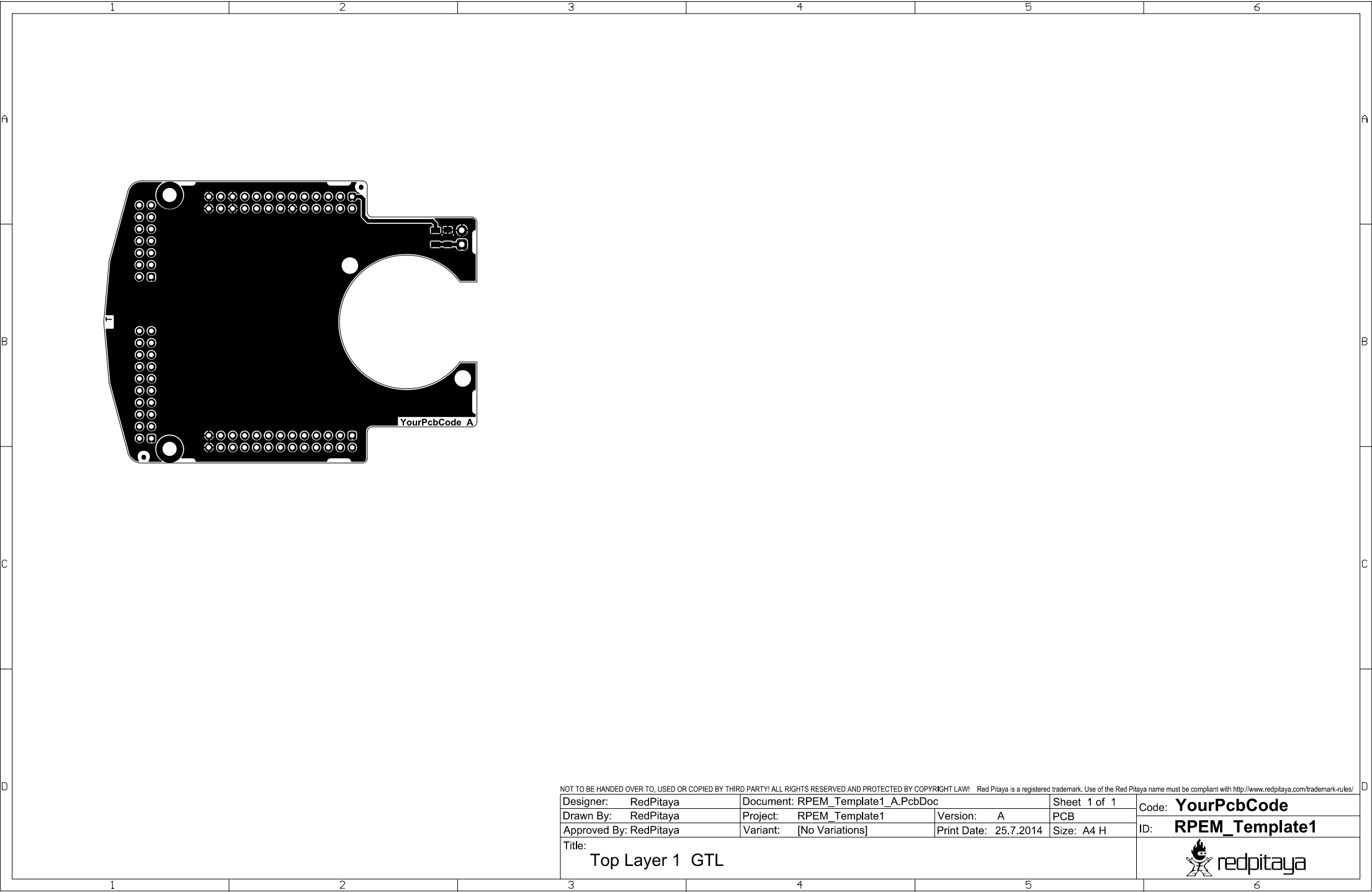
Layer Stack Up Detail for: RPEM_Template1_A.PcbDoc

Layer Name	Gerber Document	Copper Thickness	Dielectric Height	Dielectric Material	Dielectric Constant	Dielectric Type
Top Solder Mask	(.GTS)		0.0102mm	Solder Resist	3.50	
Layer1_Top	(.GTL)	0.035mm				
Layer2_Bottom	(.GBL)	0.035mm	1.55mm		4.6	Core
Bottom Solder Mask	(.GBS)		0.0102mm	Solder Resist	3.50	


Symbol	Hit Count	Tool Size	Plated	Hole Type
○	2	0.3mm <11.811mil>	PTH	Round
▽	30	0.6mm <23.622mil>	NPTH	Round
☆	86	1mm <39.37mil>	PTH	Round
⊗	2	1.2mm <47.244mil>	PTH	Round
⊕	2	3mm <118.11mil>	NPTH	Round
⊗	2	3mm <118.11mil>	PTH	Round
124 Total				

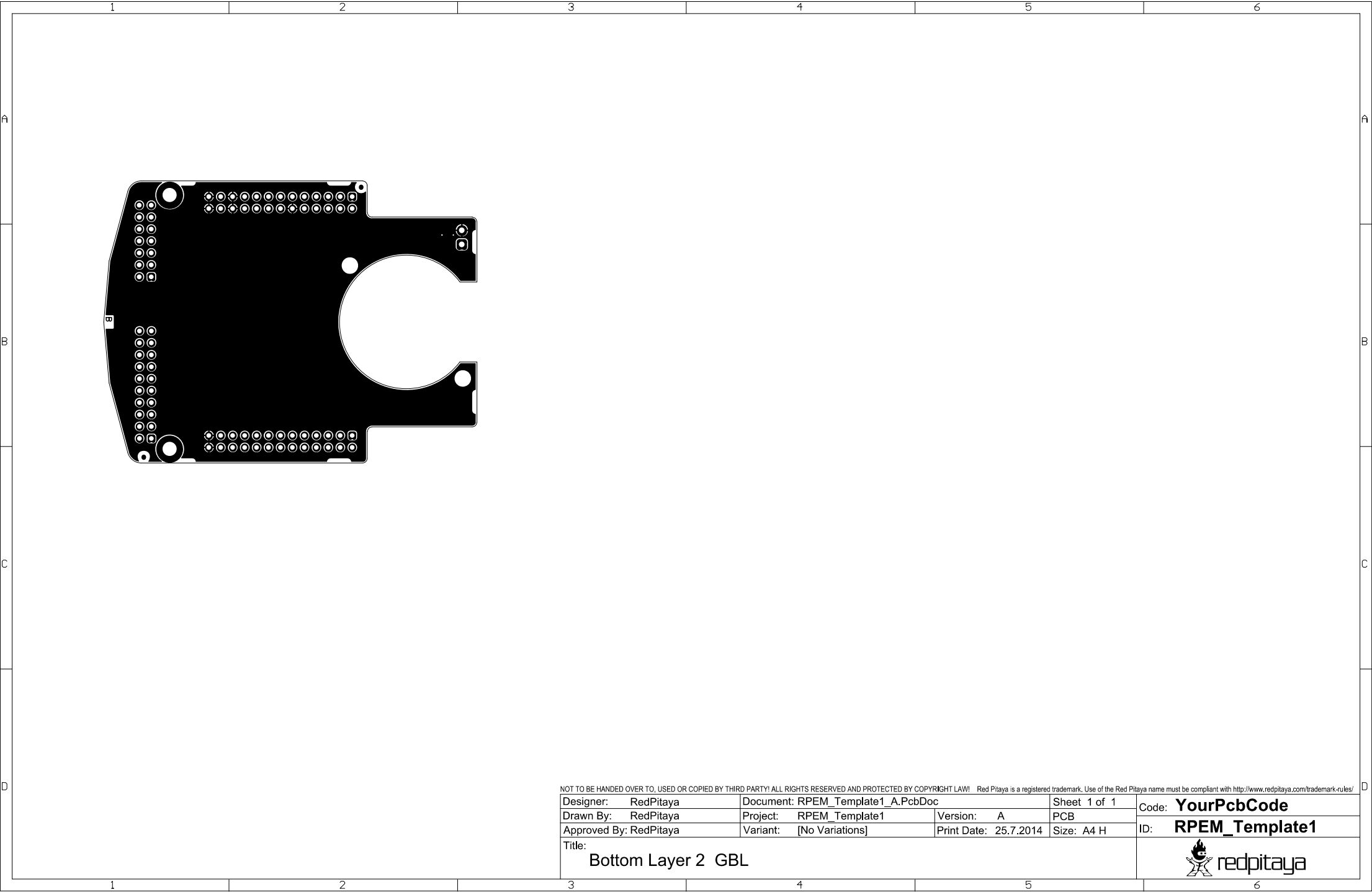
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Designer: RedPitaya	Document: RPEM_Template1_A.PcbDoc	Sheet 1 of 1	Code: YourPcbCode
Drawn By: RedPitaya	Project: RPEM_Template1	Version: A	PCB
Approved By: RedPitaya	Variant: [No Variations]	Print Date: 25.7.2014	Size: A4 H
Title: Drill Drawing and Dimensions GD1			ID: RPEM_Template1
			 redpitaya




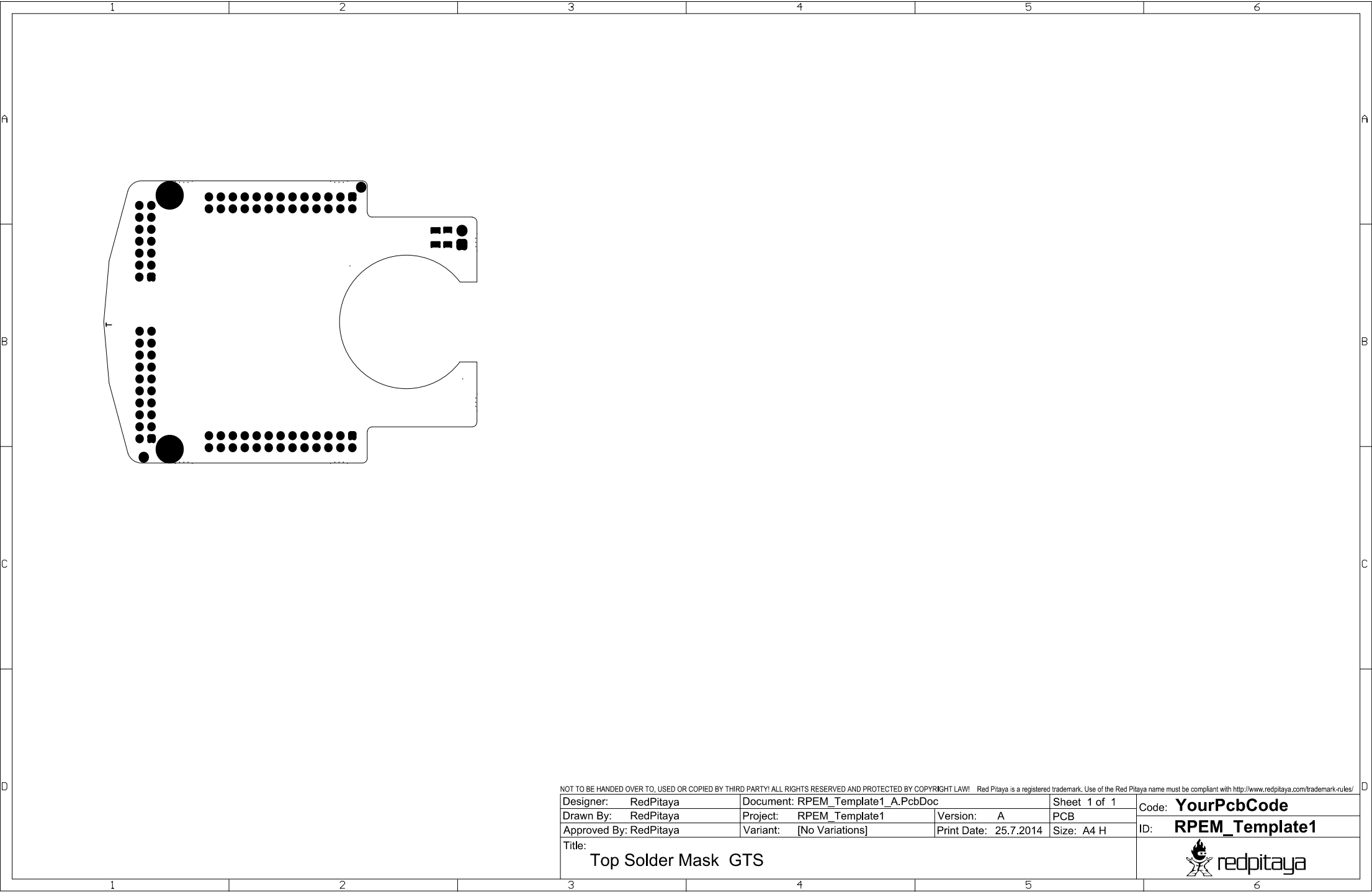
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Drawn By: RedPitaya	Project: RPEM_Template1	Version: A	PCB
Approved By: RedPitaya	Variant: [No Variations]	Print Date: 25.7.2014	Size: A4 H
Title: Top Layer 1 GTL			 redpitaya




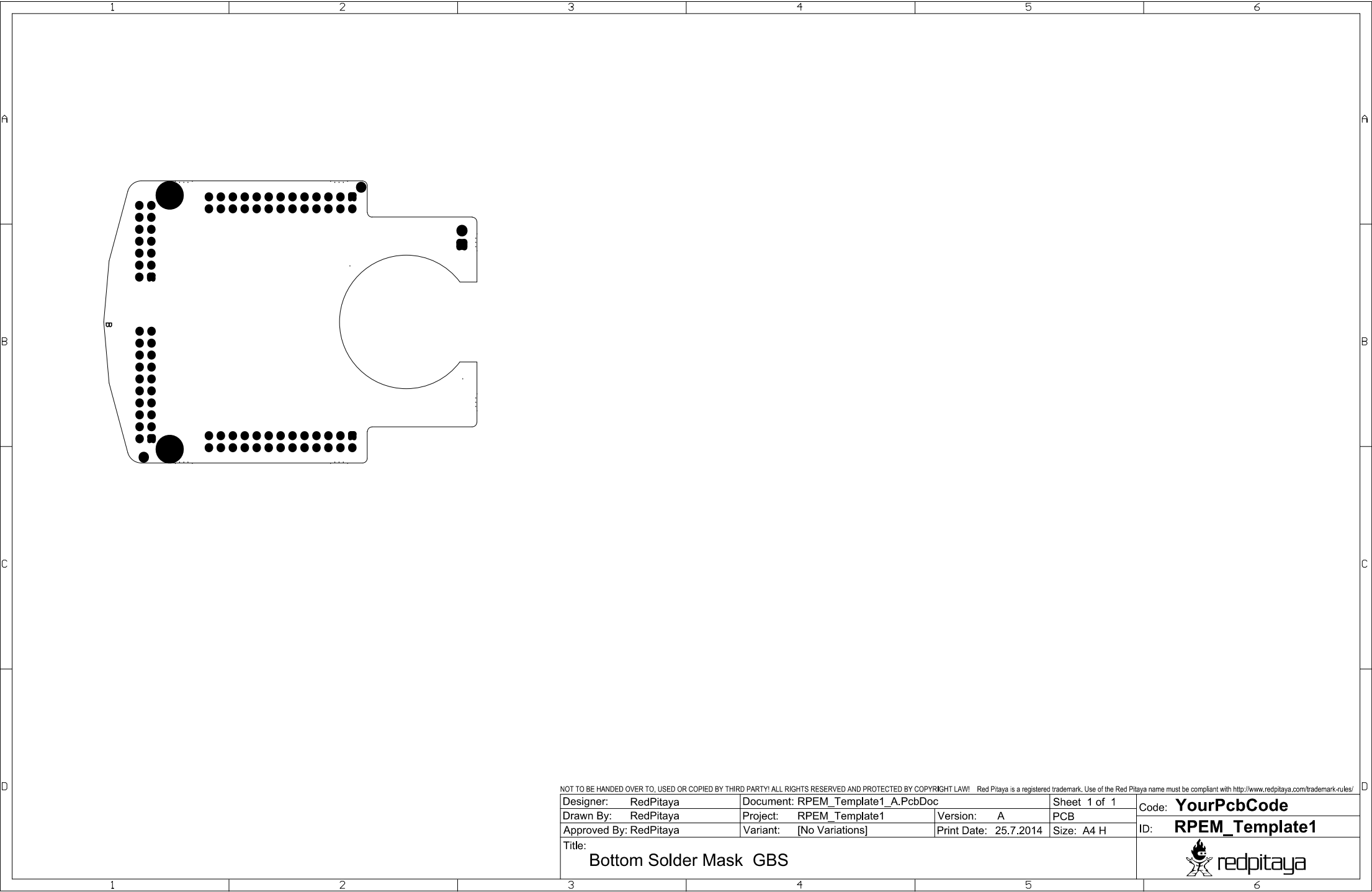
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Approved By: RedPitaya	Variant: [No Variations]	Print Date: 25.7.2014	Size: A4 H
Title: Bottom Layer 2 GBL			 redpitaya




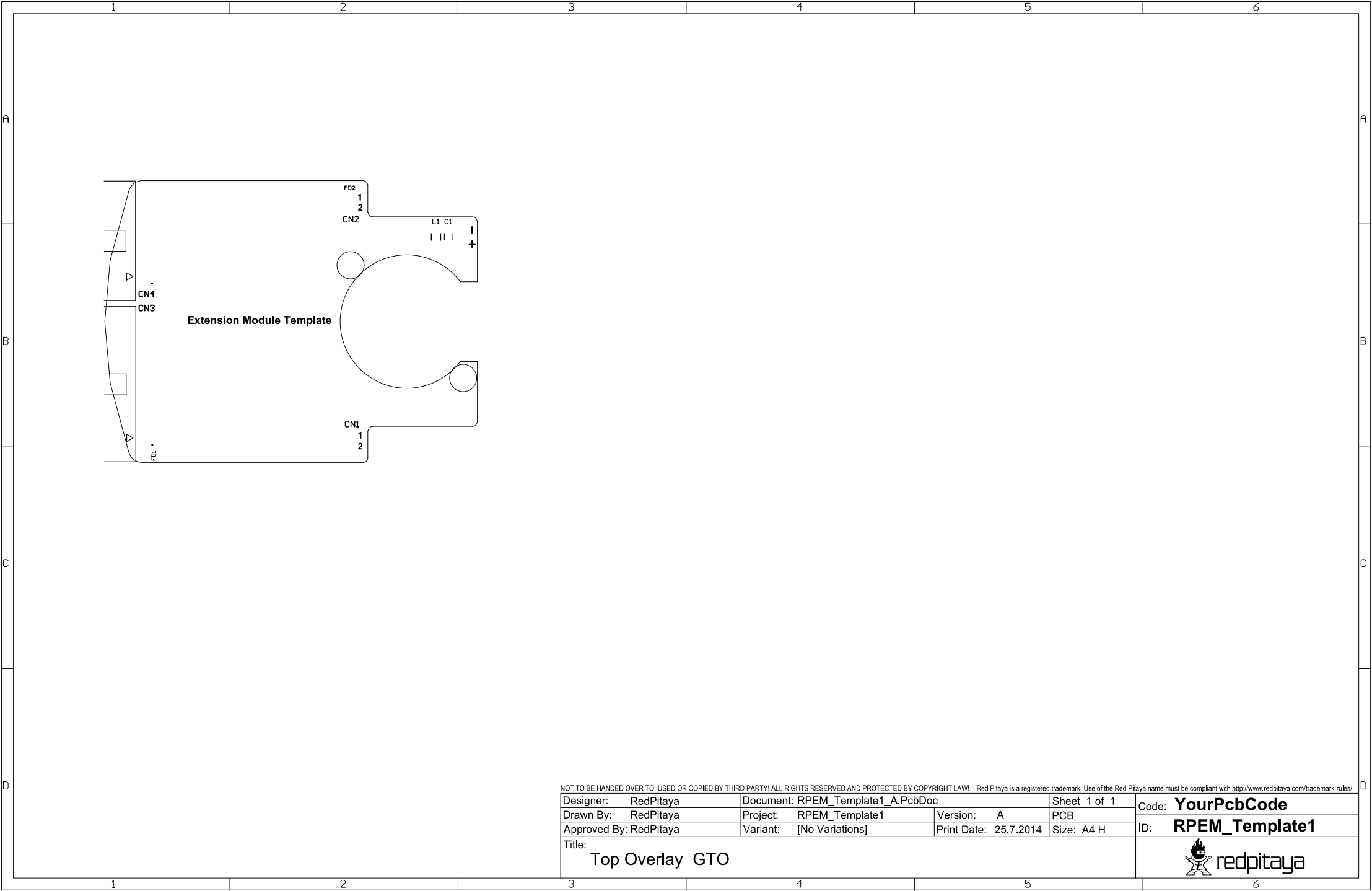
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Approved By: RedPitaya	Variant: [No Variations]	Print Date: 25.7.2014	Size: A4 H
Title: Top Solder Mask GTS			 redpitaya




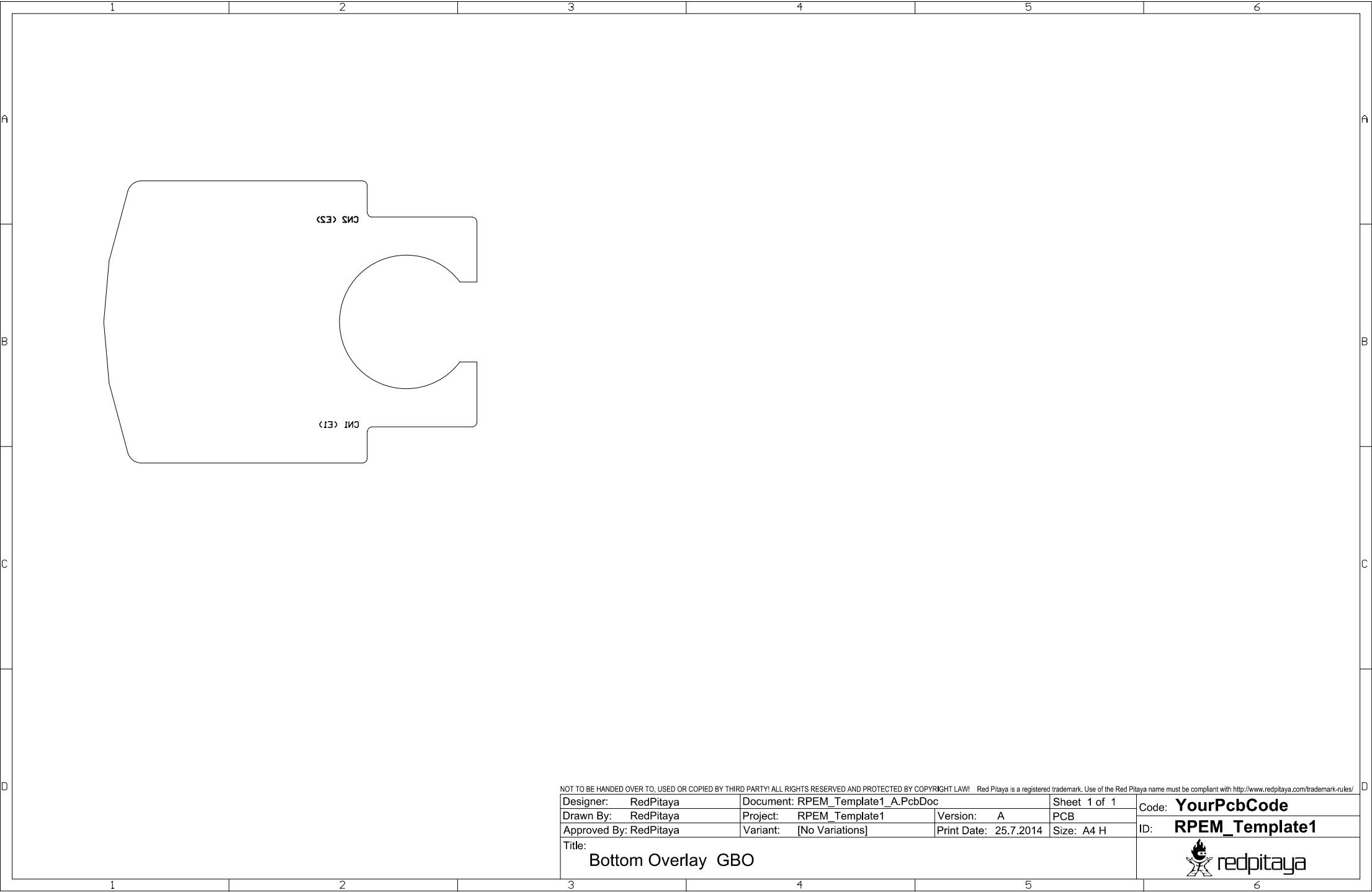
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Title: Top Overlay GTO			 redpitaya



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Approved By: RedPitaya	Variant: [No Variations]	Print Date: 25.7.2014	Size: A4 H
Title: Bottom Overlay GBO			 redpitaya